

Refine Search

Search Results -

Terms	Documents
(716/12 709/253 370/464 370/362 370/364 370/357 370/916 710/316 710/302 710/303 710/304 710/104 710/72 710/305 710/107 710/307.317 710/37 712/29 713/1 713/323).ccls.	9306

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L1	Refine Search
<input style="font-size: small; border: 1px solid black; padding: 2px 10px; margin-right: 10px;" type="button" value="Recall Text"/> <input style="font-size: small; border: 1px solid black; padding: 2px 10px;" type="button" value="Clear"/>	<input style="font-size: small; border: 1px solid black; padding: 2px 10px;" type="button" value="Interrupt"/>

Search History

DATE: Thursday, October 20, 2005 [Printable Copy](#) [Create Case](#)
SetName Queryside by
side

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

<u>L1</u>	710/316,302- 304,104,72,305,107,307.317,37;713/1,323;370/464,362,364,357,916;712/29;709/253;716/12.cccls.	<u>9306</u>	<u>L1</u>
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<u>Hit Count</u>	<u>Set Name</u>	<u>result set</u>
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END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1 and L2	76

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
Database:
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L3	<input type="button" value="Refine Search"/>
<input type="button" value="Recall Text"/> <input type="button" value="Clear"/> <input type="button" value="Interrupt"/>	

Search History

DATE: Thursday, October 20, 2005 [Printable Copy](#) [Create Case](#)

SetName Queryside by
 side

<u>Hit</u>	<u>Set</u>
<u>Count</u>	<u>Name</u>
result	set

DB=PGPB,USPT,USOC; PLUR=YES; OP=OR

L3 11 and L276 L3L2 ((first or second or third) adj1 bus) same multiplex\$3682 L2L1 710/316,302-9306 L1

304,104,72,305,107,307.317,37;713/1,323;370/464,362,364,357,916;712/29;709/253;716/12.ccls.

END OF SEARCH HISTORY

EAST - [Untitled1:1]

File View Edit Tools Window Help

- X
-
E X

Drafts Pending Active

- Drafts
- Pending
- Active
 - L1: (534) ((first or second or third) adj1 bus) same
 - L2: (17) ll same (coupl1S3 near5 selectively)
- Failed
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

Search Groups

DBs USPAT

Default operator: OR

Plurals

Highlight all hit items initially

BRS form SPC form Image Text HTML

	Type	L #	Hits	Search Text	DBs	Time	Stamp	Comment	Error	Definit	Ex
1	BRS	L1	534	((first or second or third) adj1 bus) same	USPA	2005/10/1	T 9 13:26				
2	BRS	L2	17	ll same (coupl1S3 near5 selectively)	USPA	2005/10/1	T 9 13:27				

Start » EAST - [...]

EAST - [Untitled1:1]

File View Edit Tools Window Help

[] [] X
[] [] E Drafts Pending Active L1: (534) ((first or se L2: (17) l1 same (coupl Failed Saved Favorites Tagged (0) UDC Queue TrashSearch | Help | | Home | Clear |

DBs | USPAT

Default operator: OR

 Phrases Highlight all hit terms initially

l1 same (coupl\$3 near5 selectively)

A BRIform A SCAform A Image A Text A HTML

	U	I	Document ID	Issue Date	Pages	Title	Current CR	Current X
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6928500 B1	20050809	19	High speed bus system that incorporates uni-d	710/107	710/112;
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6425029 B1	20020723	8	Apparatus for configuring bus archite	710/104	710/240;
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6405273 B1	20020611	20	Data processing device with memory coupling un	710/310	710/305
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6401151 B1	20020604	8	Method for configuring bus architecture throug	710/104	710/105;
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6363067 B1	20020326	42	Staged partitioned communication bus for a	370/364	370/402;
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6181694 B1	20010130	13	Systems and methods for multiple mode voice and	370/353	370/408;
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6101565 A	20000808	12	System for multisized bus coupling in a packe	710/307	370/458;
8	<input type="checkbox"/>	<input type="checkbox"/>	US 5918027 A	19990629	13	Data processor having bus controller	710/305	370/469
9	<input type="checkbox"/>	<input type="checkbox"/>	US 5911149 A	19990608	12	Apparatus and method for implementing a prog	711/147	370/347;
10	<input type="checkbox"/>	<input type="checkbox"/>	US 5867733 A	19990202	13	Mass data storage controller permitting d	710/74	709/215;
11	<input type="checkbox"/>	<input type="checkbox"/>	US 5644496 A	19970701	30	Programmable logic	716/17	711/130;

Start [] [] > [] EAST - [...]